

REV	Description	DATE	BY
A3	Production release.	10/21/2011	GC

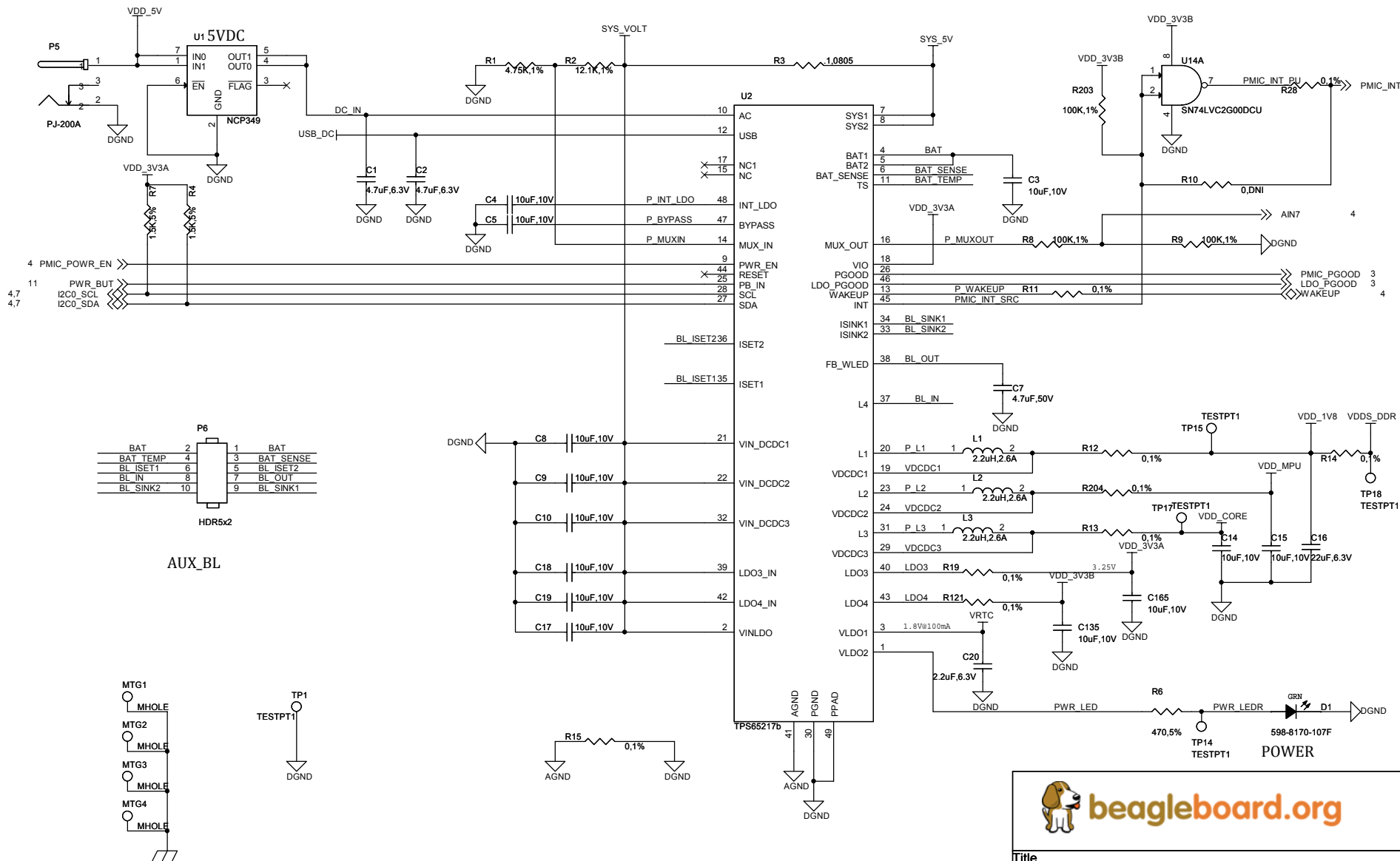
PAGE NO.	SCHEMATIC PAGE
1	COVER PAGE
2	POWER MANAGEMENT
3	PROCESSOR 1 OF 3
4	PROCESSOR 2 OF 3
5	PROCESSOR 3 OF 3
6	LED, CONFIGURATION AND BUTTON
7	MEMORY
8	ETHERNET AND USB HOST
9	USB CONCENTRATOR
10	SERIAL AND JTAG
11	EXP CONN, uSD, AND LDO

This schematic is ***NOT SUPPORTED*** and DOES NOT constitute a reference design. Only "community" support is allowed via resources at BeagleBoard.org/discuss.

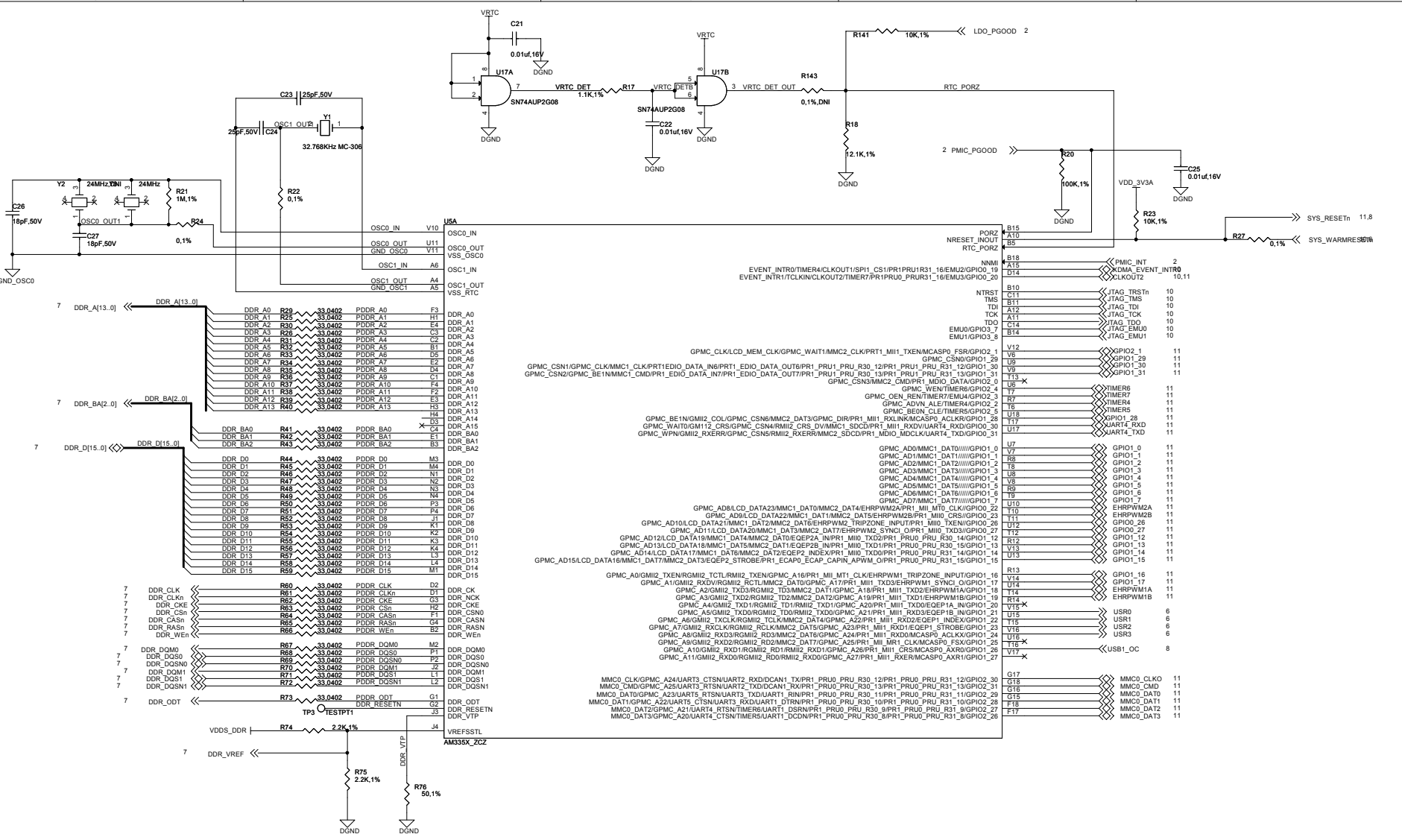
THERE IS NO WARRANTY FOR THIS DESIGN , TO THE EXTENT PERMITTED BY APPLICABLE LAW. EXCEPT WHEN OTHERWISE STATED IN WRITING THE COPYRIGHT HOLDERS AND/OR OTHER PARTIES PROVIDE THE DESIGN "AS IS" WITHOUT WARRANTY OF ANY KIND, EITHER EXPRESSED OR IMPLIED, INCLUDING, BUT NOT LIMITED TO, THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR PURPOSE. THE ENTIRE RISK AS TO THE QUALITY AND PERFORMANCE OF THE DESIGN IS WITH YOU. SHOULD THE DESIGN PROVE DEFECTIVE, YOU ASSUME THE COST OF ALL NECESSARY SERVICING, REPAIR OR CORRECTION.



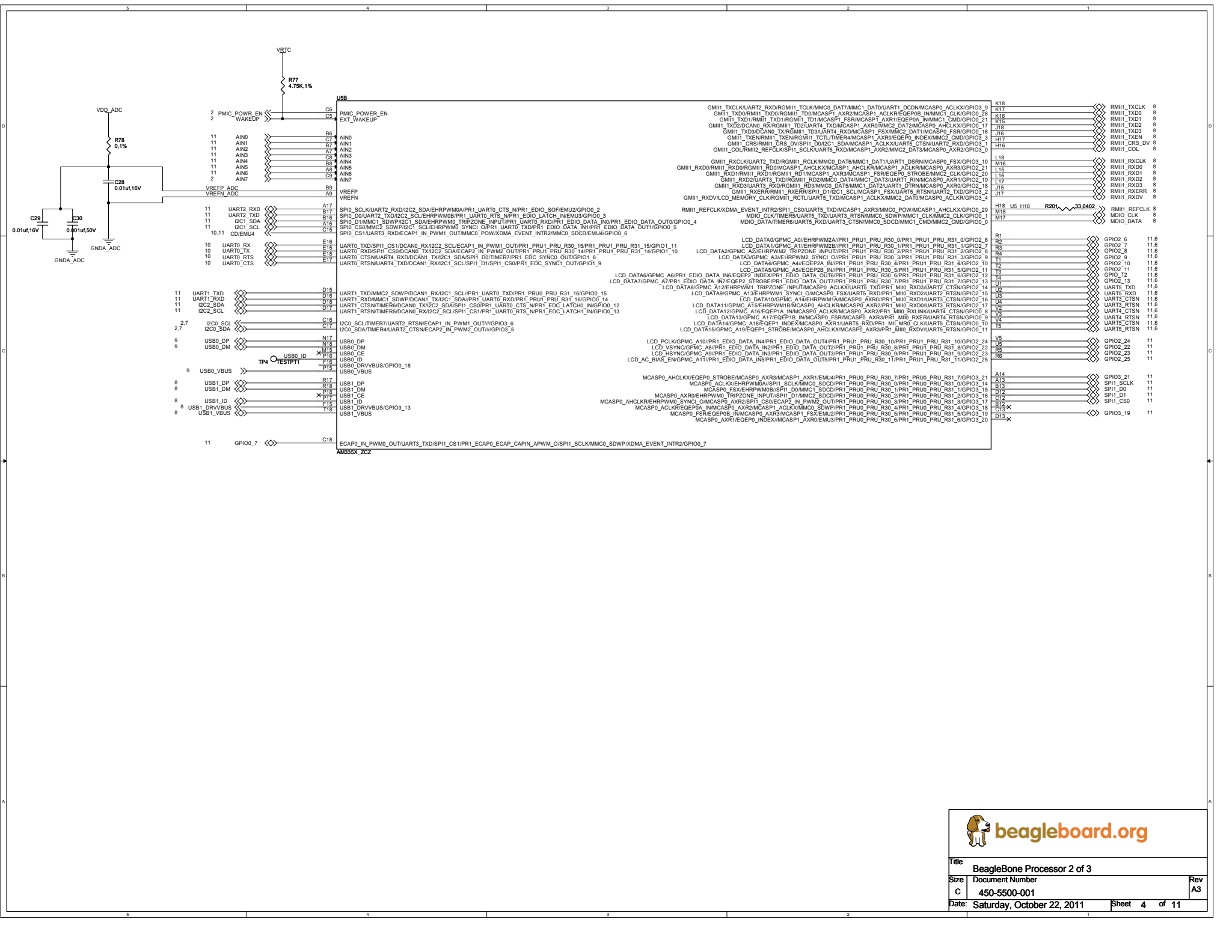
Title		BeagleBone Cover Page	
Size	Document Number	Rev	
B	450-5500-001	A3	
Date:	Saturday, October 22, 2011	Sheet	1 of 11



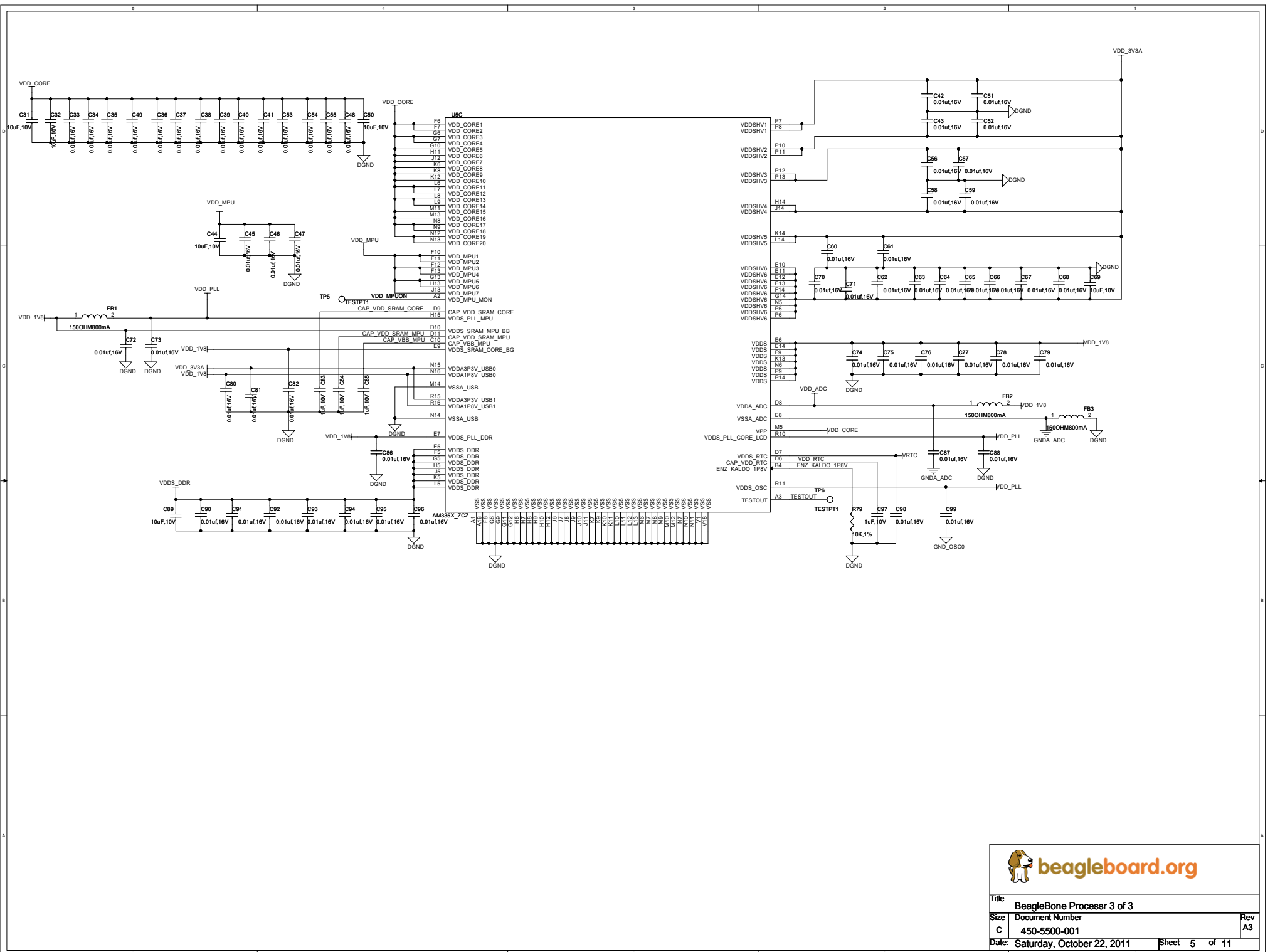
Title		BeagleBone Power Management	
Size	Document Number		Rev
B	450-5500-001		A3
Date:	Saturday, October 22, 2011	Sheet	2 of 11




Title			BeagleBone Processor 1 of 3		
Size			Document Number		
C			450-5500-001		
Date:			Saturday, October 22, 2011		
Sheet			3 of 11		
Rev			A3		

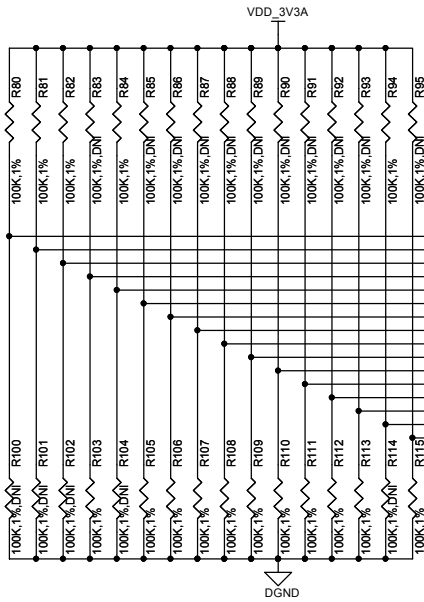


Title			BeagleBone Processor 2 of 3		
Size			Document Number		
C			450-5500-001		Rev A3
Date:			Saturday, October 22, 2011		Sheet 4 of 11




beagleboard.org

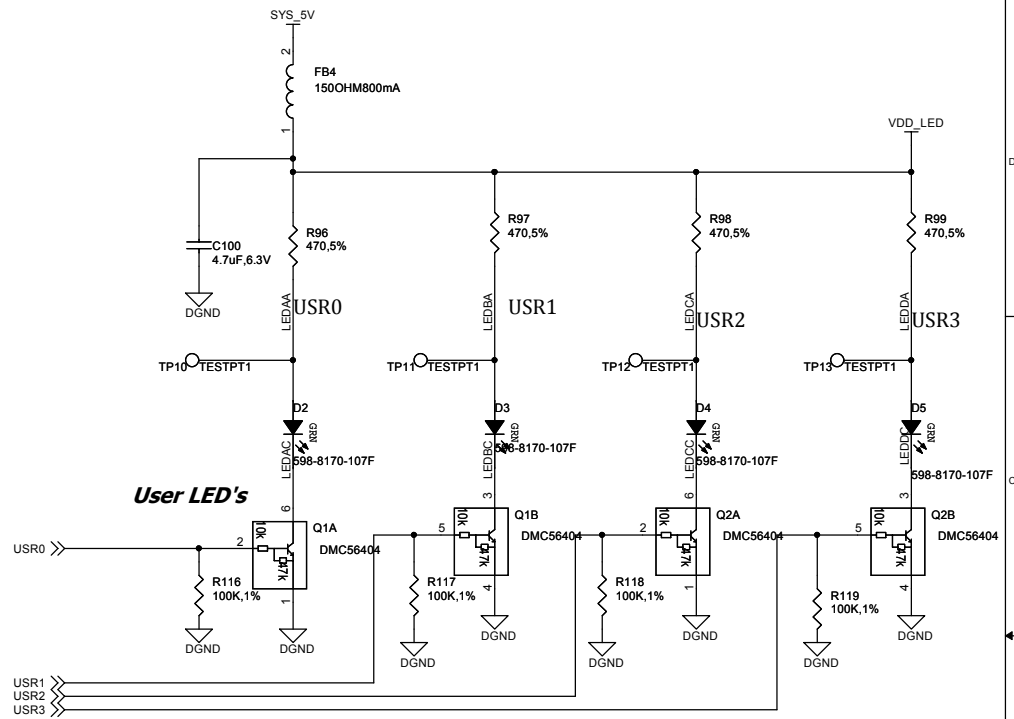
Title		
BeagleBone Processor 3 of 3		
Size		
Document Number		
C		
450-5500-001		
Date:		Rev
Saturday, October 22, 2011		A3
Sheet		of
5		11



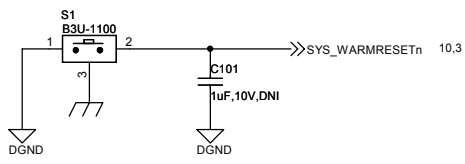
SYS_BOOT0	GPIO2_6	11,4
SYS_BOOT1	GPIO2_7	11,4
SYS_BOOT2	GPIO2_8	11,4
SYS_BOOT3	GPIO2_9	11,4
SYS_BOOT4	GPIO2_10	11,4
SYS_BOOT5	GPIO2_11	11,4
SYS_BOOT6	GPIO_12	11,4
SYS_BOOT7	GPIO2_13	11,4
SYS_BOOT8	UART5_TXD	11,4
SYS_BOOT9	UART5_RXD	11,4
SYS_BOOT10	UART3_CTSN	11,4
SYS_BOOT11	UART3_RTSN	11,4
SYS_BOOT12	UART4_CTSN	11,4
SYS_BOOT13	UART4_RTSN	11,4
SYS_BOOT14	UART5_CTSN	11,4
SYS_BOOT15	UART5_RTSN	11,4

Boot Configuration

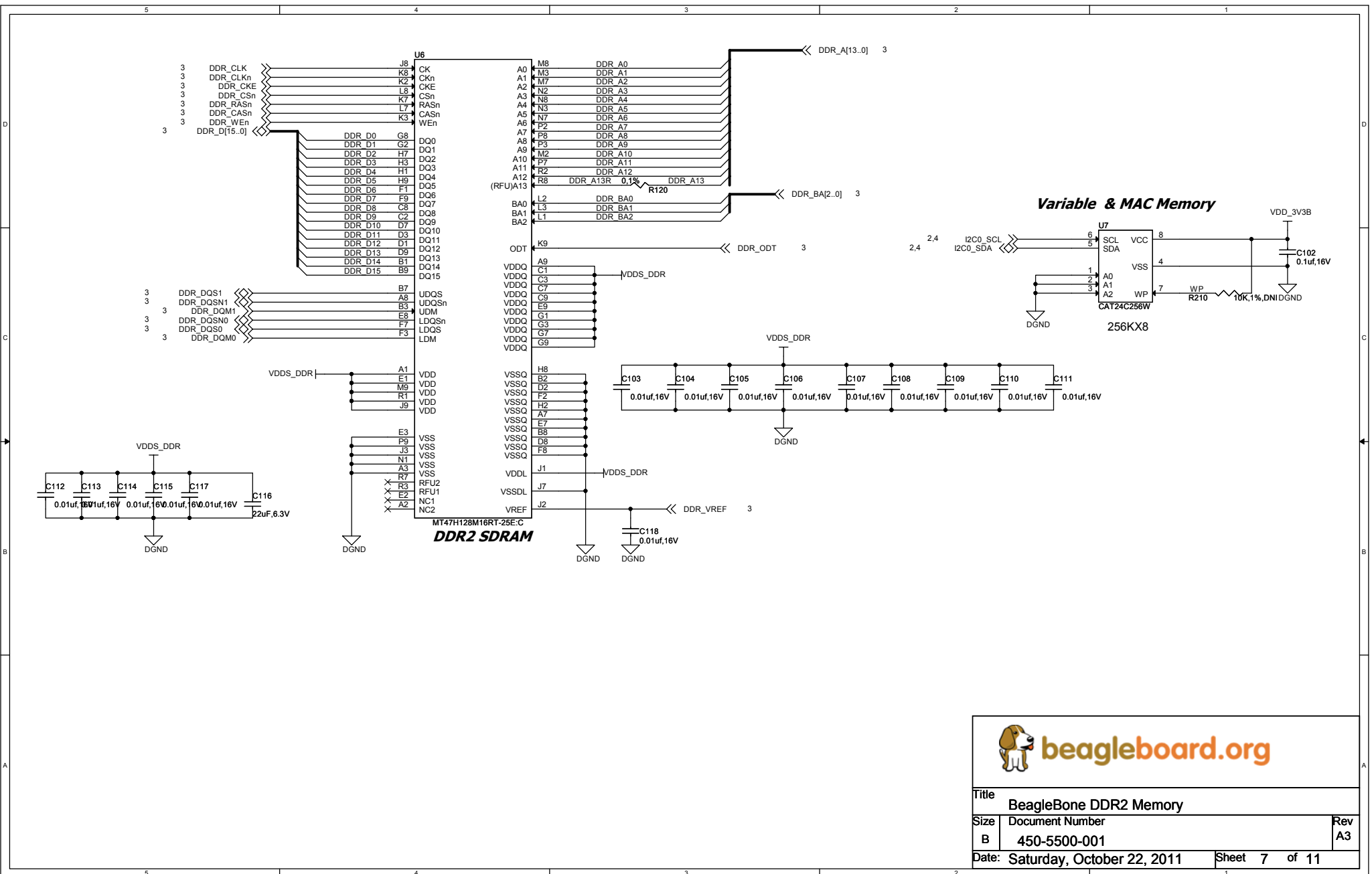
BOOT ORDER....MMC....SPI....UART....USB.....24MHZ



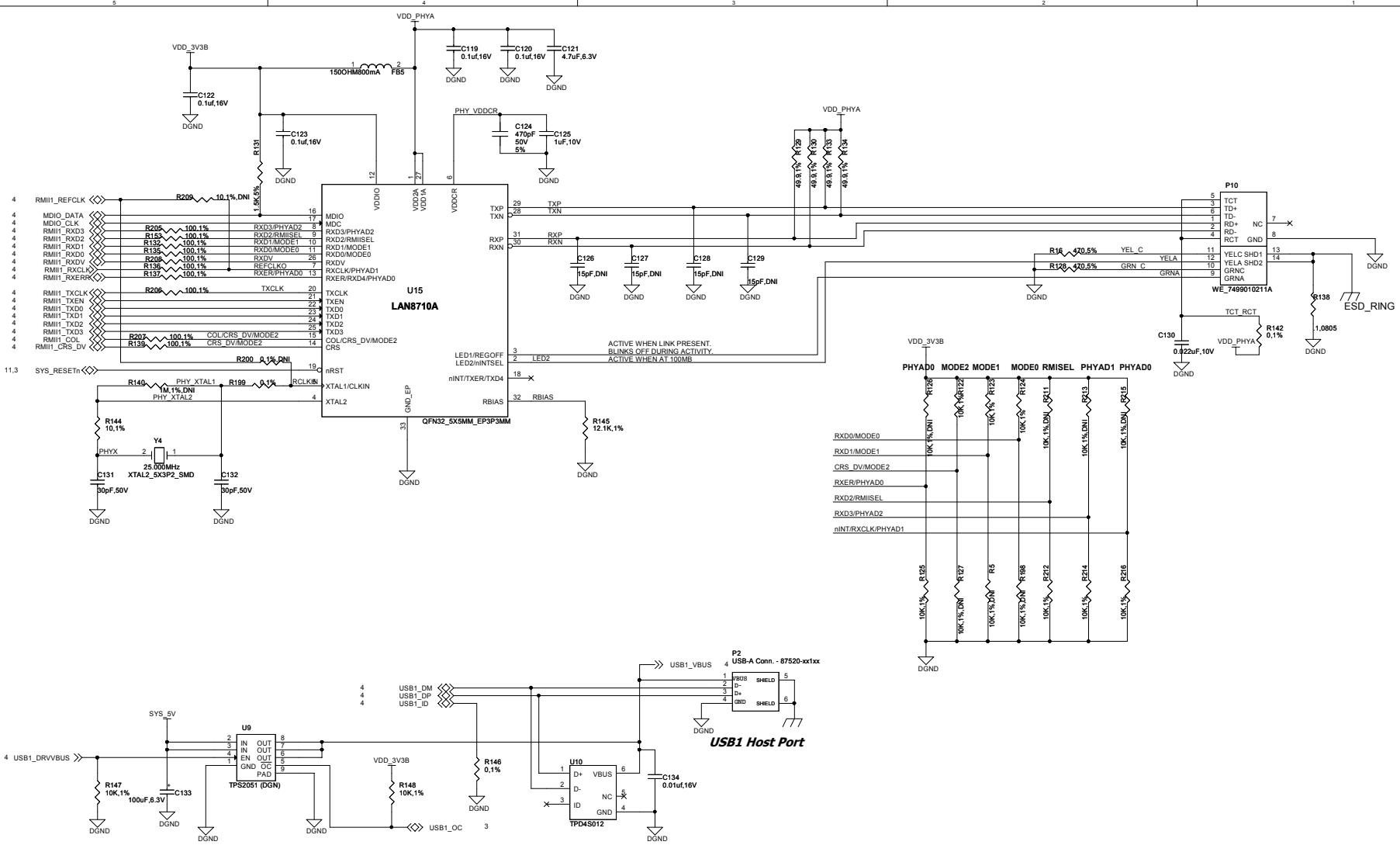
User LED's

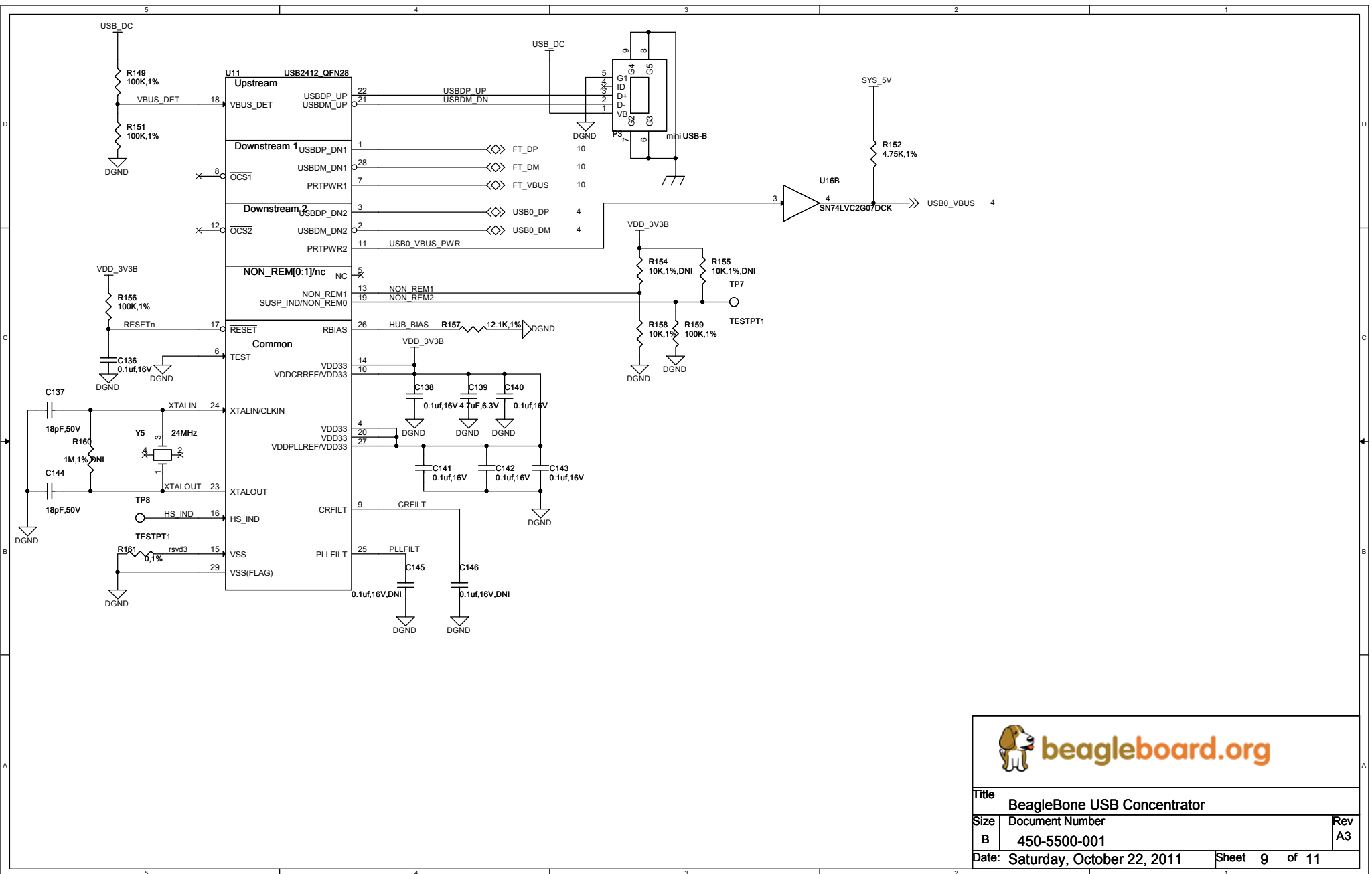


Title		
BeagleBone LED, Configuration and Reset		
Size	Document Number	Rev
B	450-5500-001	A3
Date:	Saturday, October 22, 2011	Sheet 6 of 11

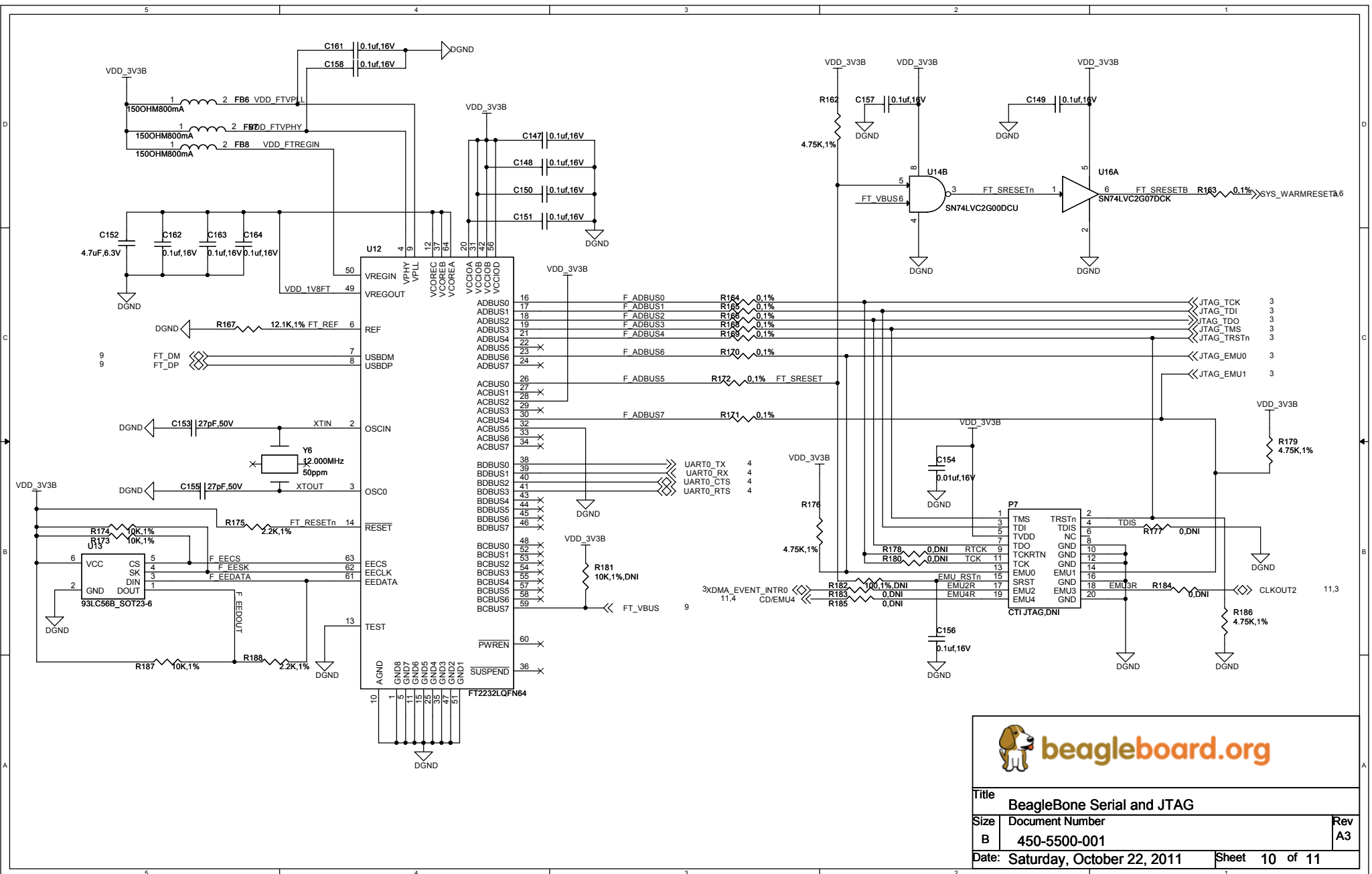


Title		BeagleBone DDR2 Memory	
Size	Document Number	Rev	
B	450-5500-001	A3	
Date:	Saturday, October 22, 2011	Sheet	7 of 11

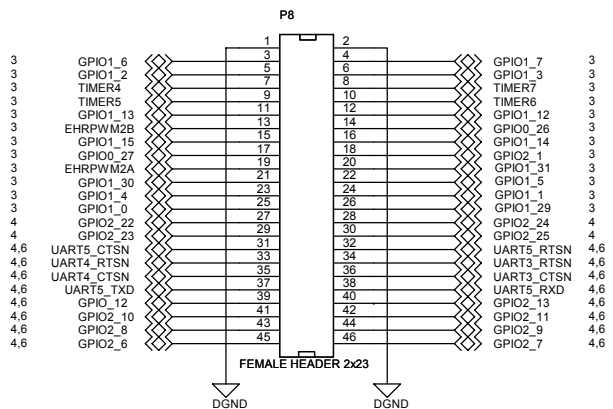




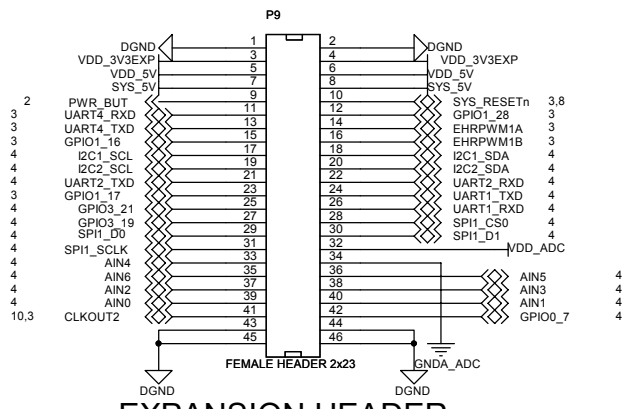
Title		BeagleBone USB Concentrator	
Size	Document Number	Rev	
B	450-5500-001	A3	
Date:	Saturday, October 22, 2011	Sheet	9 of 11



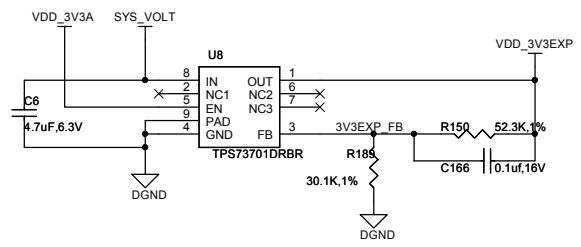
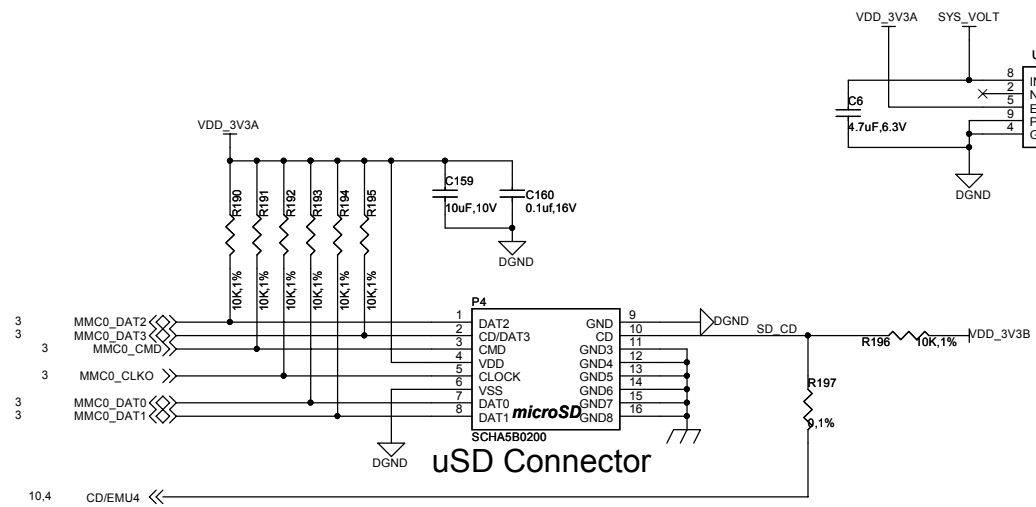
Title		
BeagleBone Serial and JTAG		
Size		Document Number
B		450-5500-001
Date:	Saturday, October 22, 2011	Sheet 10 of 11
Rev		A3



EXPANSION HEADER



EXPANSION HEADER



Title		
BeagleBone Expansion Headers, SD/MMC and LDO		
Size	Document Number	Rev
B	450-5500-001	A3
Date:	Saturday, October 22, 2011	Sheet 11 of 11